## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

<u>Priority</u> Application Serial No	4
Priority Filing Date August 28, 200	2
nventor Luan C. Tran et a	١.
Assignee Micron Technology, Inc	<b>)</b> .
Priority Group Art Unit	4
<u>Priority</u> Examiner	S
Attorney's Docket No	5
Fitle: A Method of Forming Integrated Circuitry (As amended)	

## PRELIMINARY AMENDMENT

To:

Mail Stop Patent Application Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

From:

D. Brent Kenady

Tel. 509-624-4276; Fax 509-838-3424

Wells St. John P.S.

601 W. First Avenue, Suite 1300

Spokane, WA 99201-3817

Sir:

This is a preliminary amendment accompanying a Request for Continuation Application for the above-entitled patent application. Prior to examining the application, please enter the following amendments.

## **AMENDMENTS**

Underlines indicate insertions and strikeouts indicate deletions.

## In the Title

On the Title Page, the title has been amended as follows: Memory

Integrated Circuitry A Method of Forming Integrated Circuitry.